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Relevance scale ☐ ☐ ☐ ☐ ☐**1** ["Combining" as a compilation technique for VLIW architectures](#)

T. Nakatani, K. Ebcioglu

August 1989 **ACM SIGMICRO Newsletter , Proceedings of the 22nd annual workshop on Microprogramming and microarchitecture MICRO 22**, Volume 20 Issue 3**Publisher:** ACM Press

Full text available: pdf(1.00 MB)

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Combining is a local compiler optimization technique that can enhance the performance of global compaction techniques for VLIW machines. Given two adjacent operations of a certain class that are flow (read-after-write) dependent and that cannot be placed in the same micro-instruction, the combining technique can transform the operations so that the modified operations have no dependence. The transformed operations can be executed in the same micro-instruction, thus allowing ...

2 [The theory of parsing, translation, and compiling](#)

Alfred V. Aho, Jeffrey D. Ullman

January 1972 Book

Publisher: Prentice-Hall, Inc.

Full text available: pdf(98.28 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)**From volume 1 Preface (See Front Matter for full Preface)**

This book is intended for a one or two semester course in compiling theory at the senior or graduate level. It is a theoretically oriented treatment of a practical subject. Our motivation for making it so is threefold.

(1) In an area as rapidly changing as Computer Science, sound pedagogy demands that courses emphasize ideas, rather than implementation details. It is our hope that the algorithms and concepts present ...

3 [Program structures for parallel processing](#)

James P. Anderson

December 1965 **Communications of the ACM**, Volume 8 Issue 12**Publisher:** ACM Press

Full text available: pdf(436.89 KB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)**4** [Parallel execution of prolog programs: a survey](#)

Gopal Gupta, Enrico Pontelli, Khayri A.M. Ali, Mats Carlsson, Manuel V. Hermenegildo

July 2001 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 23 Issue 4**Publisher:** ACM Press

Full text available: pdf(1.95 MB)

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Since the early days of logic programming, researchers in the field realized the potential for exploitation of parallelism present in the execution of logic programs. Their high-level nature, the presence of nondeterminism, and their referential transparency, among other characteristics, make logic programs interesting candidates for obtaining speedups through parallel execution. At the same time, the fact that the typical applications of logic programming frequently involve irregular computation ...

Keywords: Automatic parallelization, constraint programming, logic programming, parallelism, prolog

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IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

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